FORM PTO - 1449 INFORMATION DISCLOSURE STATEMENT	ATTORNEY DOCKET NO.: CDS-007 APPLICANTS: Bellantoni et al. SERIAL NO.: 10/820,643 FILING DATE: 8-Apr-2004 GROUP: 2123

U.S. PATENT DOCUMENTS

EXAM. INIT.		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
RG	A1	5,062,067	29-Oct-91	Schaefer et al.	~~364~~ 703	~~378~~ 16	15-Mar-89
RG	A2	5,437,037	25-Jul-95	Furuichi	~~395~~ 717	~~709~~ 146	7-Jun-93
RG	A3	5,502,661	26-Mar-96	Glunz	~~364~~ 703	~~378~~ 14	7-Oct-93
RG	A4	5,544,067	6-Aug-96	Rostoker et al.	~~364~~ 703	~~488~~ 14	14-Jun-93
RG	A5	5,696,942	9-Dec-97	Palnitkar et al.	~~395~~ 703	~~600~~ 17	24-Mar-95
RG	A6	5,768,567	16-Jun-98	Klein et al.	~~395~~ 703	~~600~~ 13	14-May-96
RG	A7	5,784,593	21-Jul-98	Tseng et al.	~~395~~ 703	~~600~~ 15	29-Sep-95
RG	A8	5,809,283	15-Sep-98	Vaidyanathan et al.	~~395~~ 703	~~600~~ 16	29-Sep-95
RG	A9	5,862,361	19-Jan-99	Jain	~~395~~ 703	~~600~~ 16	7-Sep-95
RG	A10	5,880,975	9-Mar-99	Mangelsdorf	~~364~~ 703	~~378~~ 15	5-Dec-96
RG	A11	5,978,571	2-Nov-99	Grundmann	~~395~~ 703	~~600~~ 23	12-May-97
RG	A12	5,991,523	23-Nov-99	Williams et al.	~~395~~ 716	~~600.10~~ 18	18-Mar-97
RG	A13	6,052,524	18-Apr-00	Pauna	~~395~~ 703	~~600.43~~ 22	14-May-98
RG	A14	6,134,516	17-Oct-00	Wang et al.	703	27	5-Feb-98
RG	A15	6,135,647	24-Oct-00	Balakrishnan et al.	~~395~~ 716	~~600.10~~ 18	23-Oct-97
RG	A16	6,152,612	28-Nov-00	Liao et al.	~~395~~ 703	~~600~~ 23	9-Jun-97
RG	A17	6,175,946 B1	16-Jan-01	Ly et al.	716	4	20-Oct-97
RG	A18	6,182,258 B1	30-Jan-01	Hollander	714	739	6-Feb-98
RG	A19	6,223,144 B1	24-Apr-01	Barnett et al.	703	22	24-Mar-98
RG	A20	6,295,517 B1	25-Sep-01	Roy et al.	703	15	7-Apr-98
RG	A21	6,321,363 B1	20-Nov-01	Huang et al.	716	4	11-Jan-99
RG	A22	6,466,898 B1	15-Oct-02	Chan	703	17	12-Jan-99

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FOREIGN PATENT DOCUMENTS

EXAM. INIT.		DOCUMENT NUMBER	DATE	COUNTRY CODE	CLASS	SUB CLASS	FILING DATE	ABSTRACT ONLY	ENGLISH LANG (Y/N)

OTHER ART, JOURNAL ARTICLES, ETC.

EXAM. INIT.		OTHER DOCUMENTS: (Including Author, Title, Date, Relevant Pages, Place of Publication)
RG	C1	Andrews, J., Axis Systemss Inc., "Co-verification speeds SOC design," EDN, September 5, 2002, pp. 95-96, 98, 100.
RG	C2	~~Axis Systems, Inc, "Xpert™ ARM® Solution," Datasheet, www.AxisSystems.com, 2 pages.~~
RG	C3	Becker, M., "Faster Verilog Simulations Using a Cycle Based Programming Methodology," *Proceedings 1996 IEEE International Verilog HDL Conference*, Feb. 26-28, 1996, Santa Clara, CA, USA: IEEE Computer Society Press, 1996, pp. 24-31.
RG	C4	~~Bell, G., "Solidification – Static Functional Verification with Solidify," HDAC, Inc., 6 pages.~~
RG	C5	Björklund, D. et al., "A Language for Multiple Models of Computation," *Proceedings of the Tenth International Symposium on Hardware/Software Codesign*, May 6-8, 2002, Estes Park, CO, USA: CODES 2002, 2002 ACM, pp. 25-30.
RG	C6	Braun, G. et al., "Using Static Scheduling Techniques for the Retargeting of High Speed, Compiled Simulators for Embedded Processors from an Abstract Machine Description," *International Symposium on System Synthesis*, Sept. 30-Oct. 3, 2001, Montreal, Quebec, Canada: 2001 ACM, pp. 57-62.
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RG	C8	Choi, K. et al., "Incremental-in-time algorithm for digital simulation" Proceedings of the Design Automation Conference. Anaheim, June 12-15, 1988, Proceedings of the Design Automation Conference. Anaheim (DAC), New York, IEEE, US, vol. CONF. 25, 12-Jun-1988, pages 501-505.
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RG	C26	Iyer, A. et al., "Power and Performance Evaluation of Globally Asynchronous Locally Synchronous Processors," *Proceedings of the 29th International Symposium on Computer Architecture (ISCA '02)*, May 25-29, 2002, Anchorage, AK, USA: IEEE Computer Society 2002, pp. 158-168.
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